| Ref<br># | Hits | Search Query                                                                                                                             | DBs      | Default<br>Operator | Plurals | Time Stamp       |
|----------|------|------------------------------------------------------------------------------------------------------------------------------------------|----------|---------------------|---------|------------------|
| L1       | 3    | ((current adj mirror and resistor\$1) and (NMOS or NMOSFET or N adj1 channel) and load and supply adj voltage and emitter and base).clm. | US-PGPUB | OR                  | OFF     | 2005/10/04 14:34 |